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CONFIRMATION NO. 8350

SERIAL NUMBER 10/550,652	FILING OR 371(c) DATE 12/07/2005 RULE	CLASS 257	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. 125426	
APPLICANTS Satoshi Sugahara, Kanagawa, JAPAN; Tomohiro Matsuno, Tokyo, JAPAN; Masaaki Tanaka, Saitama, JAPAN;					
** CONTINUING DATA ***** This application is a 371 of PCT/JP04/04379 03/26/2004					
** FOREIGN APPLICATIONS ***** JAPAN 2003086499 03/26/2003					
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged Examiner's Signature _____ Initials _____		STATE OR COUNTRY JAPAN	SHEETS DRAWING 10	TOTAL CLAIMS 45	INDEPENDENT CLAIMS 5
ADDRESS 25944					
TITLE Reconfigurable logical circuit using transistor having spi-dependent transmission characteristics					
FILING FEE RECEIVED 2930	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		